



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Toshinori NAKAYAMA

Group Art Unit: 2811

Application No.: 09/626,146

Examiner: O. Nadav

Filed: July 26, 2000

Docket No.: 106364

For: SUBSTRATE FOR SEMICONDUCTOR DEVICE, SEMICONDUCTOR CHIP MOUNTING SUBSTRATE, SEMICONDUCTOR DEVICE AND METHOD OF FABRICATION THEREOF, AND CIRCUIT BOARD, TOGETHER WITH ELECTRONIC EQUIPMENT

**LARGE ENTITY REQUEST FOR
CONTINUED EXAMINATION UNDER 37 C.F.R. §1.114**

Director of the U.S. Patent and Trademark Office
Washington, D.C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. §1.114, Applicant(s) hereby request(s) continued examination and entry and consideration of the October 23, 2002 submission. The above-identified application was filed on or after June 8, 1995. Thus, entry is proper under 37 C.F.R. §1.114(d).

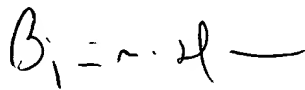
Attached hereto is our check No. 137317 in the amount of ☒ \$740.00 as payment of the fees set forth in 37 C.F.R. §1.17(e). The Director is hereby authorized to charge any additional fees or credit any overpayment associated with this communication to Deposit Account No. 15-0461. Two duplicate copies of this page are enclosed.

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Respectfully submitted,



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Date: December 18, 2002

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